

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	93	703/14.ccls. and @pd>"20071201"	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/05/09 18:25
L3	12	ioput	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/05/09 18:32
L4	78	bidirectional adj node\$1	US-PGPUB; USPAT; EPO; DERWENT	OR	OFF	2008/05/09 18:33



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ioput

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**Scholar** All articles - Recent articles

Results 1 - 10 of about 137 for ioput. (0.11 seconds)

Did you mean: input

Efficient Learning of Non-Uniform B-Splines for Modelling and Control - all 8 versions »

J ZHANG, A KNOLL, I RENNERS - Computational Intelligence for Modelling, Control & ..., 1999 - books.google.com

... The universe of discourse of each **ioput** is divided into a number of subintervals, where each subinterval is delimited by breakpoints called knots which ...

Cited by 5 - Related Articles - Web Search

Voltage level translation for an output driver system with a bias generator - all 2 versions »

F Hinedi, M Cases, S Dutta, RH Dennard - US Patent 5,986,472, 1999 - freepatentsonline.com

... A gate of transistor 118 is connected to bias circuit 130 at Node E. A drain of transistor 118 is connected to an information node **IOPUT**, which is connected to ...

Cited by 3 - Related Articles - Cached - Web Search

Stacked PFET off-chip driver with a latch bias generator for overvoltage protection - all 3 versions »

F Hinedi, L Mamileti - US Patent 6,141,200, 2000 - freepatentsonline.com

... Transistor P2 is connected at the drain to an output node connected, through resistor R, to output terminal **IOPUT**. Also connected ...

Related Articles - Cached - Web Search

Multi-Agent-Systems Based on Coloured Petri Nets - all 7 versions »

D Moldt, F Wienberg - Lecture Notes in Computer Science: 18th International ..., 1997 - Springer

... with the Object-ID. A place **ioput** liin0t l 176 obj id\* l method 1 l "

l method 2 "i r ..... methodn t ..... Figure 3: Folding ...

Cited by 84 - Related Articles - Web Search - BL Direct

Concurrent Hierarchical and Multilevel Simulation of VLSI Circuits - all 3 versions »

RB Mueller-Thuns, JT Rahmeh, JA Abraham, JA Wehbeh ... - SIMULATION, 1993 - sim.sagepub.com

Page 1. http://sim.sagepub.com SIMULATION DOI: 10.1177/003754979306000202

1993; 60; 79 SIMULATION Robert B. Mueller-Thuns, Joseph ...

Related Articles - Web Search - BL Direct

On the structure and the adjustment of the all-fiber isolator

Z Xiaopin, Y Peida - TENCON 90. 1990 IEEE Region 10 Conference on Computer and ..., 1990 - ieeexplore.ieee.org

... **ioput** Polarization (PI) CPCL) - polar:zcr l controller Fig.3 The SOP evolution for the tilt ... **ioput** Polarization (PI) CPCL) - polar:zcr l controller ...

Web Search

Evaluating the European Commission RTD Programmes: Responding to the

IKL O'SULLIVAN, GFL DURIEUX, Y DUMONT - Science Evaluation and Its Management, 1999 - books.google.com

... and reports available in time to be used as **ioput** to policy decisions; ii1 Better harmonisation: a common coherent approach to the evaluation of individual ...

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iopot analog

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**Scholar** All articles - Recent articles

Results 1 - 10 of about 14 for iopot analog. (0.08 seconds)

Did you mean: input analog

DONALD: a workbench for interactive design space exploration and sizing of analog circuits - all 3 versions »

K Swings, W Sansen - Design Automation. EDAC. Proceedings of the European ..., 1991 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)  
... circuit sizing. The program is de to numerically invert an analytic design model of analog circuit in any arbitrary direction. It ...

Cited by 23 - [Related Articles](#) - [Web Search](#)

Design methodology and simulation tools for mixed analog-digital integrated circuits

R Beale, R Chadha, CF Chen, A Prosser, KM Tham - Circuits and Systems, 1990., IEEE International Symposium on, 1990 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

... floating capacitances form a group. A behavioral analog model is added to the adjoining group if it is connected at the iopot terminals with that group. ...

Cited by 1 - [Related Articles](#) - [Web Search](#)

Multilevel verification of MOS circuits - all 3 versions »

D Weise - Computer-Aided Design of Integrated Circuits and Systems, ..., 1990 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

... the schematic of an MOS VLSI circuit, declarations of the logical relationships between the iopot signals, and a ... Circuit components operate on analog signals. ...

Cited by 3 - [Related Articles](#) - [Web Search](#)

M 3-a multilevel mixed-mode mixed A/D simulator - all 4 versions »

R Chadha, C Visweswariah, CF Chen - Computer-Aided Design of Integrated Circuits and Systems, ..., 1992 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

... IEEE, and Chin-Fu Chen, Member, IEEE Abstract—This paper describes a unified multilevel mixed-mode simulation capability for mixed analog/digital integrated ...

Cited by 9 - [Related Articles](#) - [Web Search](#)

NETHDL: abstraction of schematics to high-level HDL - all 2 versions »

D Fischer, Y Levhari, G Singer - Design Automation Conference, 1990. EDAC. Proceedings of the ..., 1990 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

... Conventional circuit (analog) simulators (SPICE [1], ASTAP [23]) are not suited for the ... type, size and strength of devices, and INPUT/OUTPUT/ IOPUT pins for ...

Cited by 4 - [Related Articles](#) - [Web Search](#)

A systematic approach to the control of electrohydraulic servosystems - all 2 versions »

A Alleyne - American Control Conference, 1998. Proceedings of the 1998, 1998 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

... zero. Hence the output tracking error,  $0, u u' I O p u t e n = -k, e, (2.22)$

$e n - r + l = y - Y_{desired}$ , converges to zero as  $t \rightarrow \infty$ . The ...

Cited by 12 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

A CMOS rectifier-integrator for amplitude detection in hard disk servo loops - all 4 versions »

MSJ Steyaert, W Dehaene, J Craninckx, M Walsh, P ... - Solid-State Circuits, IEEE Journal of, 1995 - [ieeexplore.ieee.org](http://ieeexplore.ieee.org)

... Belgium. M. Walsh and P. Real are with Analog Devices By.. Raheen Industrial Estate, Limerick, Ireland. IEEE Log Number 9412405. Fig. ...



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bidirectional analog digital

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[In vivo bidirectional color Doppler flow imaging of picoliter blood volumes using optical coherence ... - all 11 versions »](#)

JA Izatt, MD Kulkarni, S Yazdanfar, JK Barton, AJ ... - Optics Letters, 1997 - OSA  
... Letter we describe the application of this technique to **bidirectional** blood flow mapping in ... BPF, bandpass filter; A/D's, **analog-digital** converters. ...

Cited by 196 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[High speed bidirectional digital cable transmission system - all 6 versions »](#)

TM Paff, H Graham - US Patent 5,553,064, 1996 - freepatentsonline.com  
... said QPSK modulated data, and a **digital to analog** ... converting said QPSK modulated to **analog** form for ... 2. The high speed **bidirectional** cable transmission system ...

Cited by 20 - [Related Articles](#) - [Cached](#) - [Web Search](#)

[A single-fiber bidirectional optical link using colocated emitters and detectors - all 3 versions »](#)

J Cross, A Buchanan, B Carastro, LSCW Jokerst, NH ... - Photonics Technology Letters, IEEE, 1996 -  
ieeexplore.ieee.org

... These colocated devices enable single fiber **bidirectional** communication between the two **analog/digital** silicon circuits that contain the emitted detector pairs. ...

Cited by 14 - [Related Articles](#) - [Web Search](#) - [BL Direct](#)

[Superconducting analog-to-digital converter with grounded four-junction squid bidirectional counter - all 6 versions »](#)

GS Lee - US Patent 5,019,818, 1991 - freepatentsonline.com  
... 4646060, February, 1987, Phillips et al. 340/347AD, Superconducting **analog-to-digital** converter with **bidirectional** counter. 4672359, ...

Cited by 10 - [Related Articles](#) - [Cached](#) - [Web Search](#)

[Bidirectional digital serial interface for communication digital signals including digitized audio ... - all 3 versions »](#)

US Patent 5,007,050, 1991 - freepatentsonline.com  
... The Interface System is duplex (**bidirectional**). ... between the Control Unit and the Transceiver Unit, having separate **analog** audio, **digital** data and control ...

Cited by 26 - [Related Articles](#) - [Cached](#) - [Web Search](#)

[Cellular telephone for monitoring analog and digital control channels - all 2 versions »](#)

JEAS Dahlin, WGA Mueller, W Ghisler - US Patent 5,420,911, 1995 - freepatentsonline.com  
... The **bi-directional** multi-mode mobile station can use either **analog** or **digital** traffic channels and **analog** or **digital** control channels. ...

Cited by 26 - [Related Articles](#) - [Cached](#) - [Web Search](#)

[SEPIA: a cooperative hypermedia authoring environment - all 11 versions »](#)

N Streit, J Haake, J Hannemann, A Lemke, W Schuler ... - Proceedings of the ACM conference on Hypertext, 1993 - portal.acm.org

... SEPIA provides a **digital** audio channel for au- dio-only conferencing as well as an **analog** audio/video conferencing device enabling currently two – soon up to ...

Cited by 251 - [Related Articles](#) - [Web Search](#)